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501.43253X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. TAKAHASHI, et al.
Application No.: 10/722,461
Filed: November 28, 2003
For: SEMICONDUCTOR MEMORY DEVICE AND
SEMICONDUCTOR INTEGRATED CIRCUIT
Art Unit: Unknown
Examiner: Unknown

SUBMISSION OF SUPPLEMENTAL DECLARATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 24, 2004

Sir:

In supplement to the Declaration previously filed in this application, Applicants are submitting herewith a Supplemental Declaration executed by the inventors providing the correct priority date of November 29, 2002 for the Japanese priority document 2002-347998, for which priority is hereby claimed. It is noted that the original Declaration filed with this application inadvertently contained an error with regard to the Japanese filing date for the priority documents. Accordingly, entry of the present Supplemental Declaration for correctly claiming priority with the correct priority date of November 29, 2002 is respectfully requested. With regard to this, it is noted that a claim for priority with the proper date of November 29, 2002 has also been filed in this application on November 28, 2003, together with a certified copy of the Japanese Priority Application No. 2002-347998.

If the Examiner believes that there are any other points which may be clarified or otherwise disposed of either by telephone discussion or by personal interview, the Examiner is invited to contact Applicants' undersigned attorney at the number indicated below.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Docket No. 501.43253X00) please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By 
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GEM/dlt

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